


Application. No:	10/762,767
Filed:	January 20, 2004
Inventor(s):	Henry Wurzburg, Tetsuo Yamamoto, and Mark Colman Atchison
Title:	Peripheral Device Feature Allowing Processors to Enter A Low Power State



Examiner:	Lee, Seung H.
Group/Art Unit:	2876
Atty. Dkt. No:	5707-06000

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Alexandria, VA 22313-1450, on the date indicated below.

Jeffrey C. Hood

9/12  
Signature

4/25/2006  
Date

**Mail Stop Amendment**  
**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

This paper is submitted in response to the Office Action of January 25, 2006, to further highlight why the application is in condition for allowance.

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